Ruttonsha International Rectifier Ltd.

INVERTER GRADE THYRISTORS

Type : 85 RK 40F To 120F

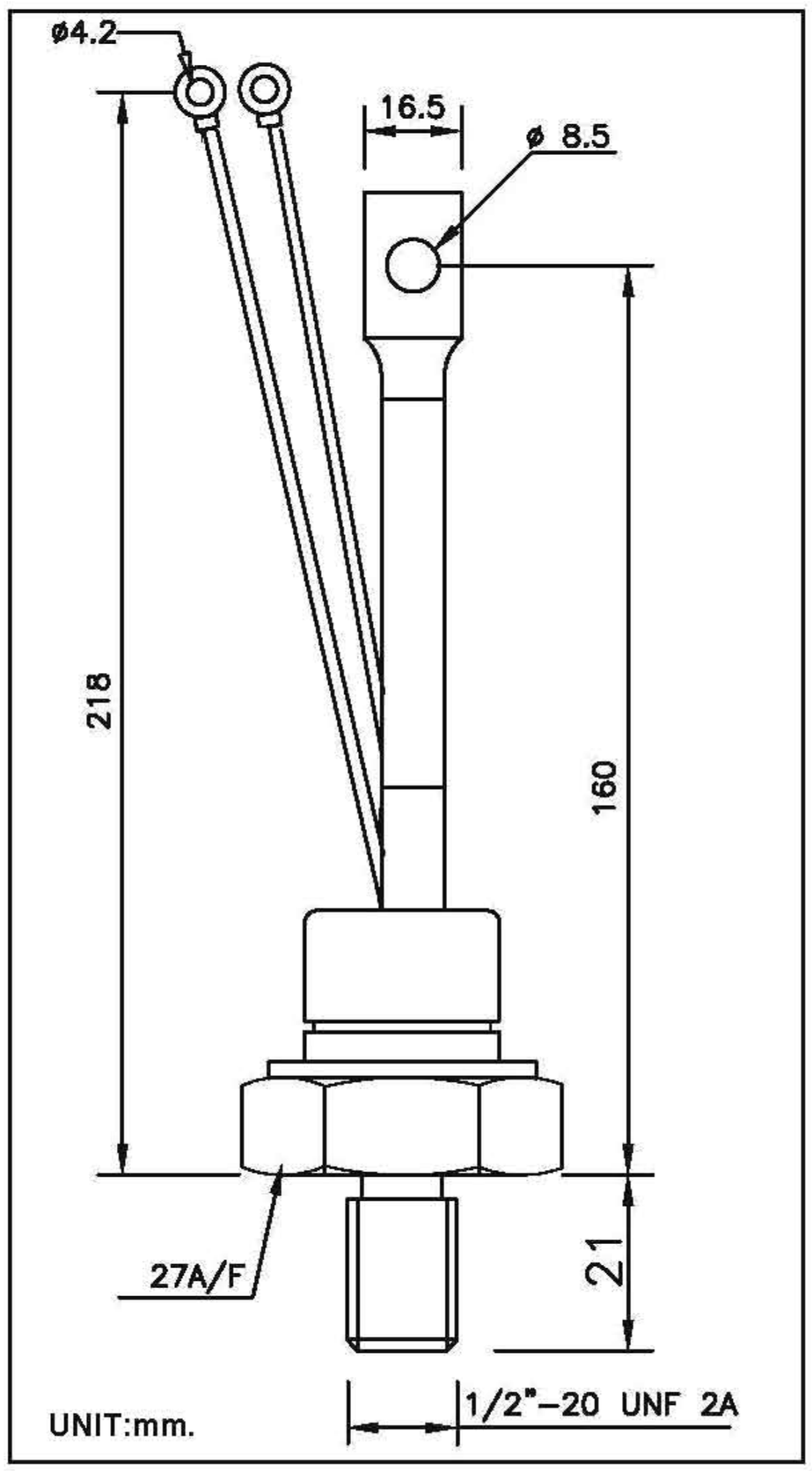
85A

Features

- All diffused design
- Center amplifying gate
- Guaranteed high dv/dt
- Guaranteed high di/dt
- High surge current capability
- Low thermal impedance
- High speed performance

Typical Applications

- Inverters
- Choppers
- Induction heating
- All types of force-commutated converters



Major Ratings and Characteristics :-

PARAMETERS	85 RK.. F	UNITS
$I_{T(AV)}$	85	A
@T_C	85	°C
$I_{T(RMS)}$	134	A
I_{TSM} @50Hz	2450	A
I^2t @50Hz	30	KA^2s
V_{DRM} /V_{RRM}	400 to 1200	V
T_q range(*)	10 to 30	µs
T_J	- 40 to 125	°C

(*) T_q = 10 to 20 µs for 400 to 800V devices
T_q = 20 to 30 µs for 1000 to 1200V devices

85 RK...F Series

ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code	V_{DRM}/V_{RRM}, max repetitive peak voltage V	V_{RSM}, maximum non-repetitive peak voltage V	I_{DRM}/I_{RRM} max. @ $T_J = T_J$ max. mA
85 RK ... F	40	400	500	30
	60	600	700	
	80	800	900	
	100	1000	1100	
	120	1200	1300	

On - state Conduction

	Parameter	85 RK..F	Units	Conditions	
$I_{T(AV)}$	Max. average on-state current	85	A	180° conduction, half sine wave	
	@ Case temperature	85	°C		
$I_{T(RMS)}$	Max. RMS on-state current	134	A	DC @ 85°C case temperature	
I_{TSM}	Max. peak, one half cycle, non-repetitive surge current	2450	A	t = 10 ms	Sinusoidal half wave, Initial $T_J=T_J$ max.
I^2t	Maximum I^2t for fusing	30	KA^2s	t = 10 ms	
$I^2\sqrt{t}$	Maximum $I^2\sqrt{t}$ for fusing	300	$KA^2\sqrt{s}$	t = 0.1 to 10 ms, no voltage reapplied	
$V_{T(TO)}$	Threshold voltage	1.46	V	$T_J=T_J$ max.	
r_t	Forward slope resistance	4.0	mΩ	$T_J=T_J$ max.	
V_{TM}	Max. peak on-state voltage	2.15	V	I_{TM} = 300A, $T_J=T_J$ max, t_P = 10 ms sine wave pulse	
I_H	Maximum holding current	600	mA	T_J = 25°C , I_T > 30A	
I_L	Typical latching current	1000		T_J = 25°C , V_A =12V, Ra = 6 Ω, I_G = 1A	

85 RK...F Series

Switching

	Parameter	85 RK...F		Units	Conditions
di/dt	Max. non-repetitive rate of rise of turned-on current	100		A/µs	T_J=T_J max., V_{DRM} = rated V_{DRM} T_{TM} = 2 x di/dt
t_d	Typical delay time	0.80		µs	T_J=25°C, V_{DM} =rated V_{DRM}, I_{TM} = 50ADC, t_p=1µs Resistive load, Gate pulse: 10V,5 Ω source
		Min	Max		
t_q	Maximum turn-off time	10	30	µs	T_J=T_J max. I_{TM}=100A,commutating di/dt = 10A/µs, V_R = 50V, t_p=200µs,dv/dt : see table in device code

Blocking

	Parameter	85 RK..F	Units	Conditions
dv/dt	Maximum critical rate of rise of off-state voltage	500	V/µs	T_J = T_J max. linear to 80% rated V_{DRM} higher value available on request
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current	30	mA	T_J = T_J max. rated V_{DRM} /V_{RRM} applied

Triggering

	Parameter	85 RK..F	Units	Conditions
P_{GM}	Maximum peak gate power	40	W	T_J = T_J max., f = 50Hz, d% = 50
$P_{G(AV)}$	Maximum average gate power	5	W	T_J = T_J max., f = 50Hz, d% = 50
I_{GM}	Max. peak positive gate current	5	A	T_J = T_J max., $t_p \leq 5$ ms
$+V_{GM}$	Maximum peak positive gate voltage	20	V	T_J = T_J max., $t_p \leq 5$ ms
$-V_{GM}$	Maximum peak negative gate voltage	5		
I_{GT}	Max. DC gate current required to trigger	200	mA	T_J = 25°C, V_A = 12V, Ra = 6 Ω.
V_{GT}	Max. DC gate voltage required to trigger	3	V	
I_{GD}	Max.DC gate current not to trigger	20	mA	T_J = T_J max., rated V_{DRM} applied
V_{GD}	Max.DC gate voltage not to trigger	0.25	V	

85 RK...F Series

Thermal and Mechanical Specifications

	Parameter	85 RK ..F	Units	Conditions
T_J	Max.junction operating temperature range	- 40 to 125	°C	
T_{stg}	Max.storage temperature range	- 40 to 150		
R_{thJC}	Max. thermal resistance, junction to case	0.195	K/W	DC operation
R_{thCS}	Max. thermal resistance,case to heatsink	0.08	K/W	Mounting surface,smooth, flat and greased
T	Mounting torque, ± 10%	15.5 (137)	Nm (lbf-in)	Non lubricated threads
		14.0 (120)	Nm (lbf-in)	lubricated threads
wt	Approximate weight	130	g	
	Case style	TO-209AC(TO-94)		

85 RK...F Series

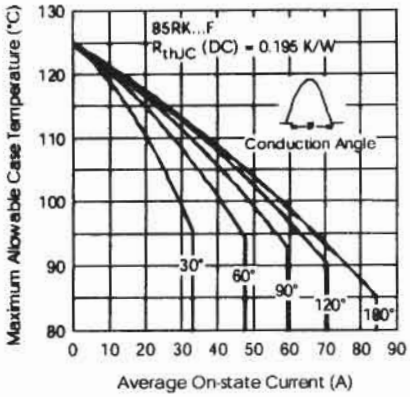


Fig. 1 - Current Ratings Characteristics

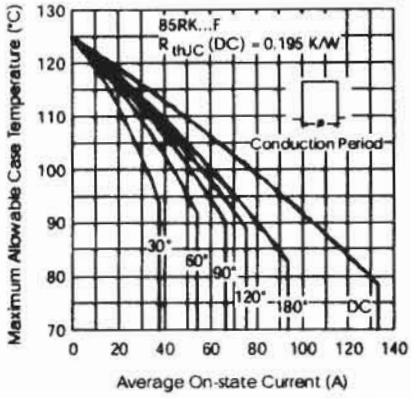


Fig. 2 - Current Ratings Characteristics

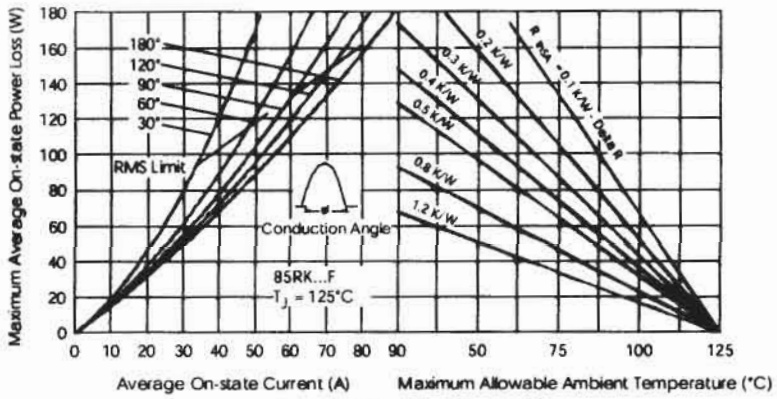


Fig. 3 - On-state Power Loss Characteristics

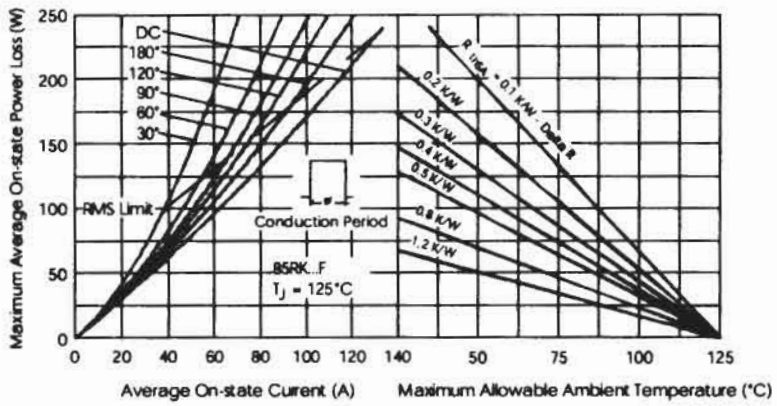


Fig. 4 - On-state Power Loss Characteristics

85 RK...F Series

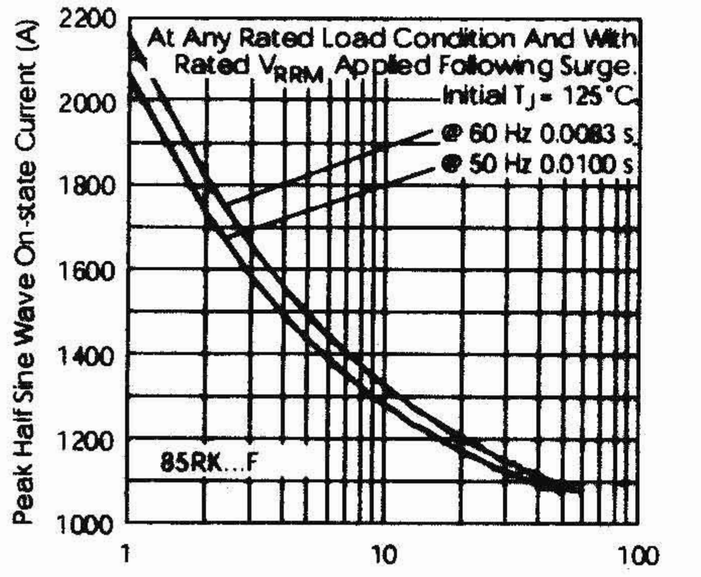


Fig. 5 - Maximum Non-repetitive Surge Current

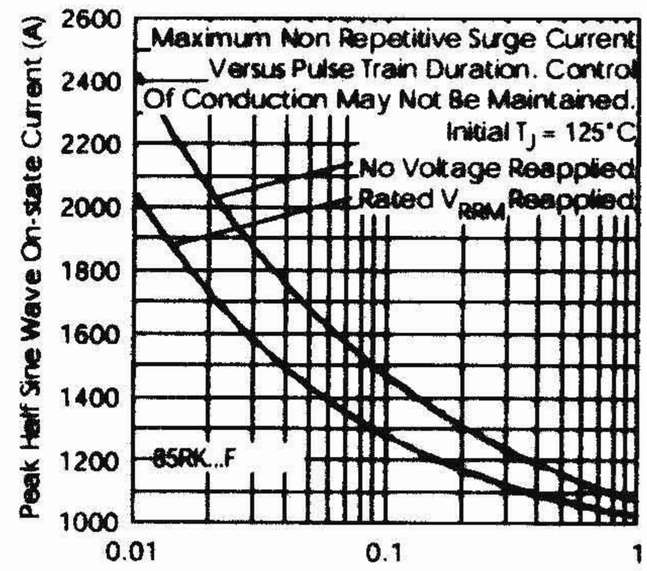


Fig. 6 - Maximum Non-repetitive Surge Current

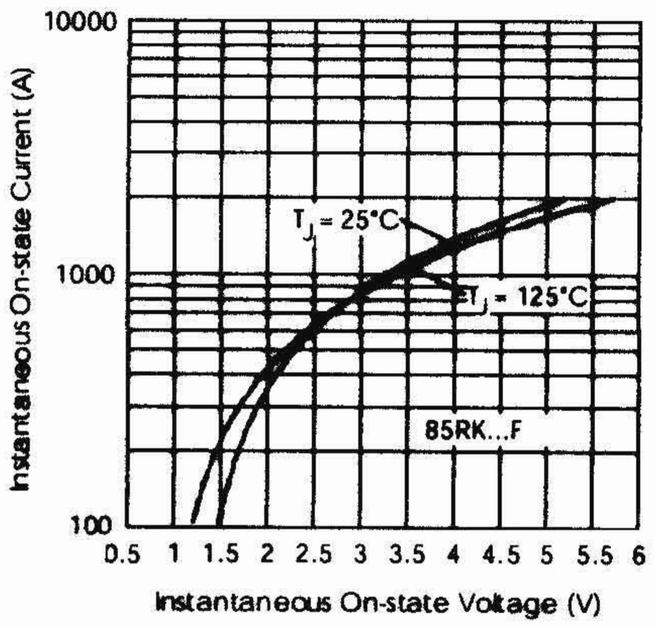


Fig. 7 - On-state Voltage Drop Characteristics

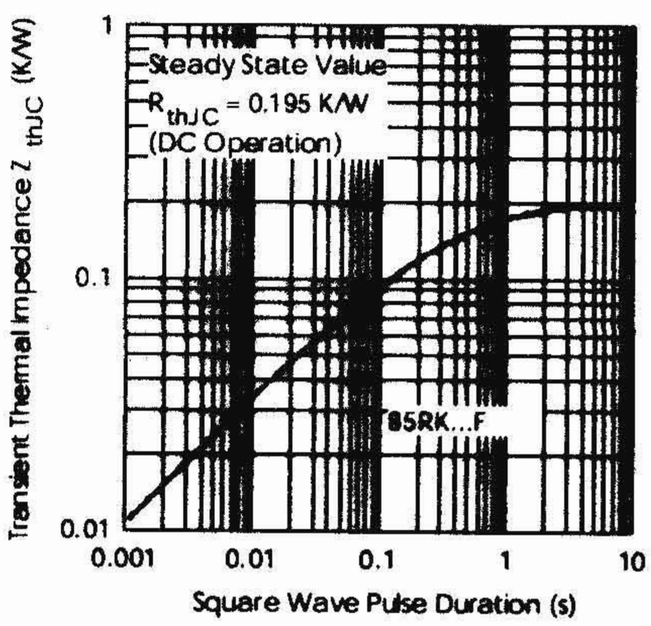


Fig. 8 - Thermal Impedance Z_{thJC} Characteristic

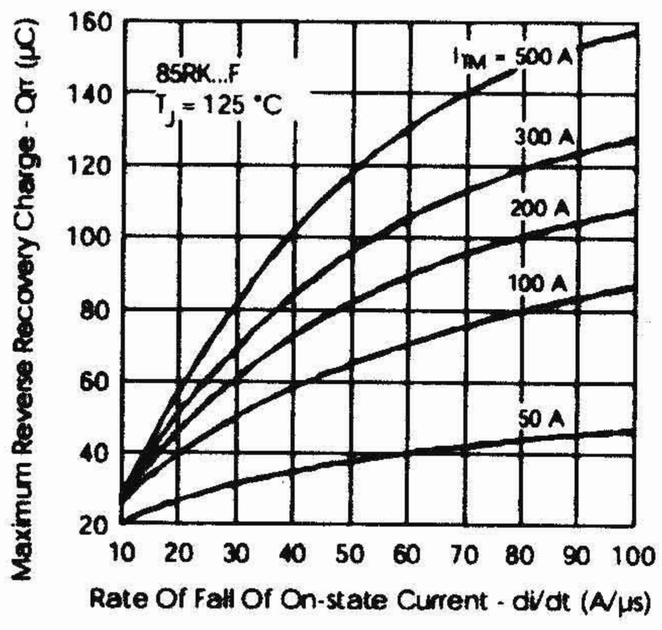


Fig. 9 - Reverse Recovered Charge Characteristics

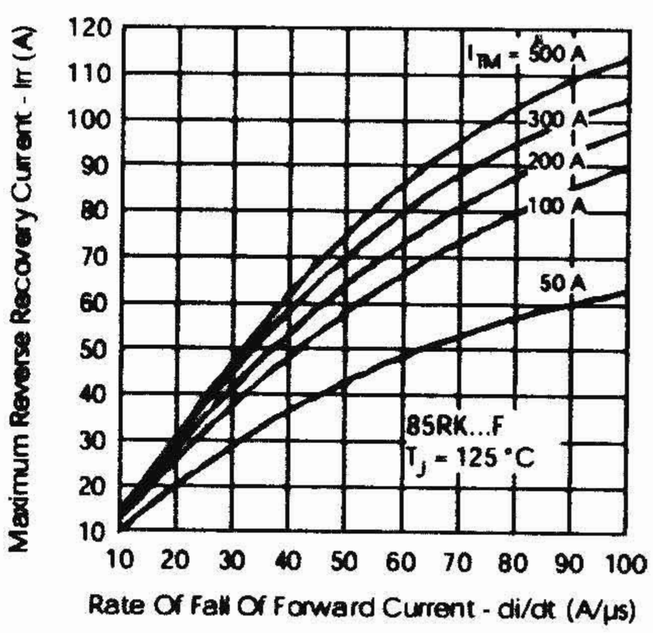


Fig. 10 - Reverse Recovery Current Characteristics

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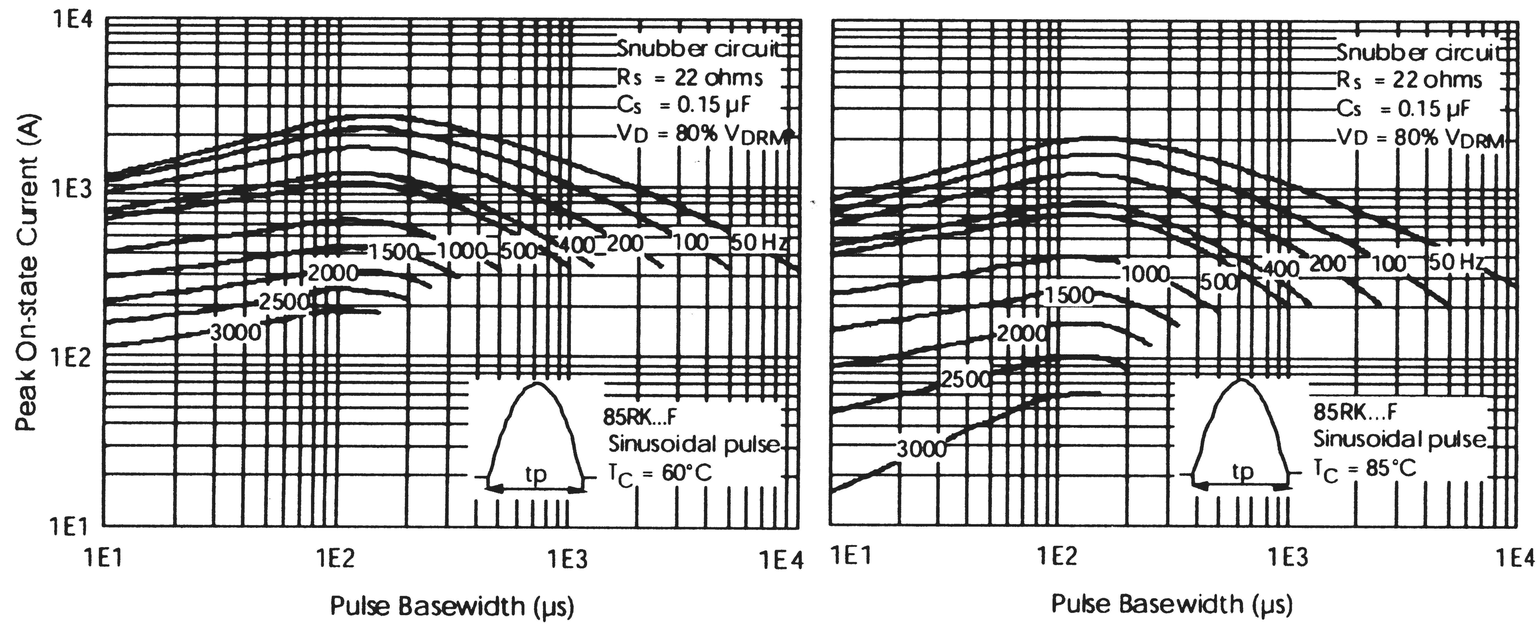

Fig. 11 - Frequency Characteristics

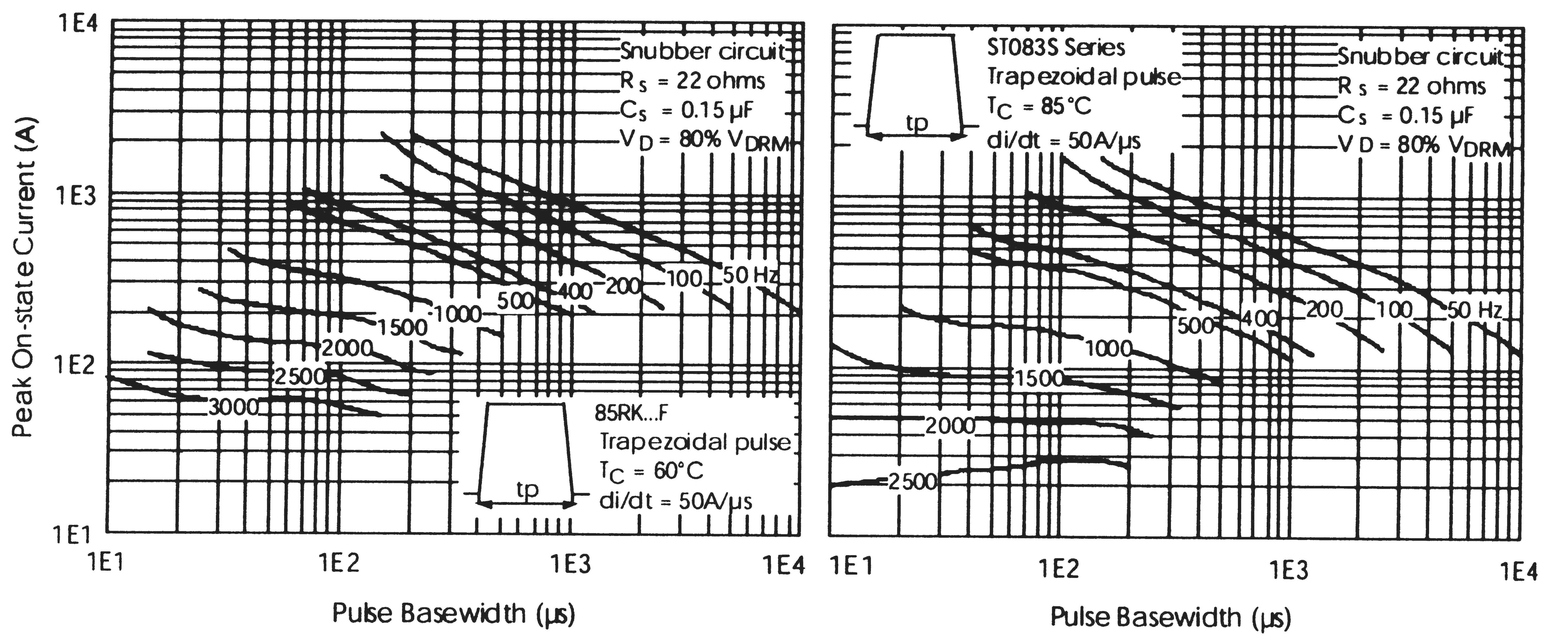

Fig. 12 - Frequency Characteristics

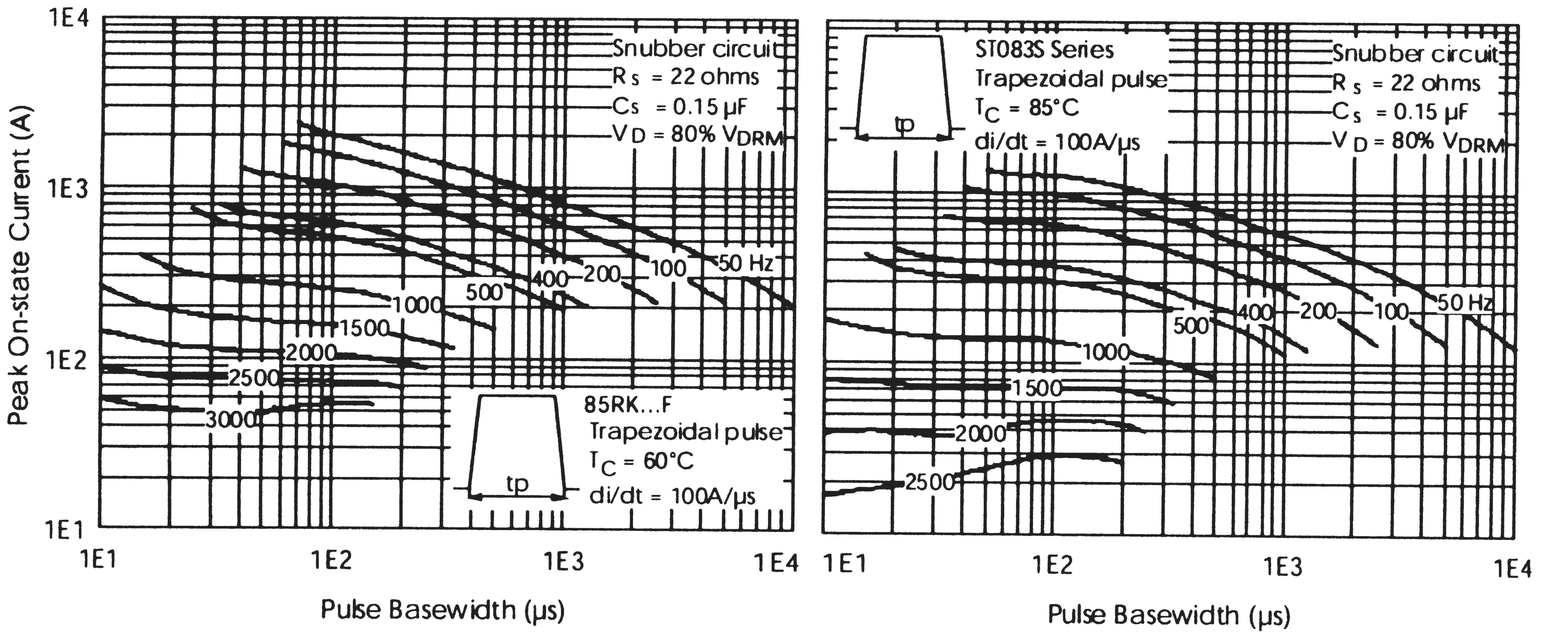

Fig. 13 - Frequency Characteristics